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FIG.1

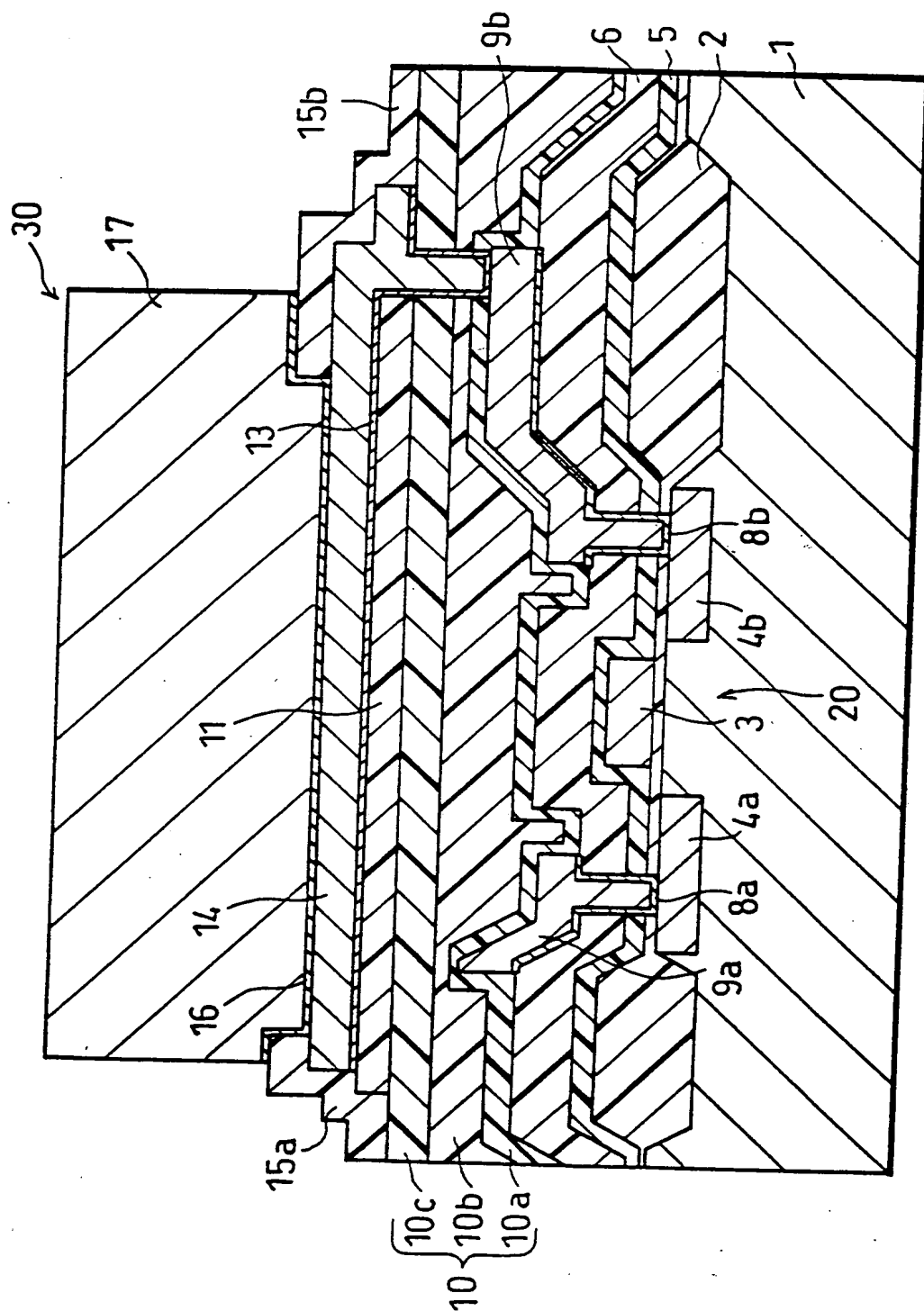


FIG. 2

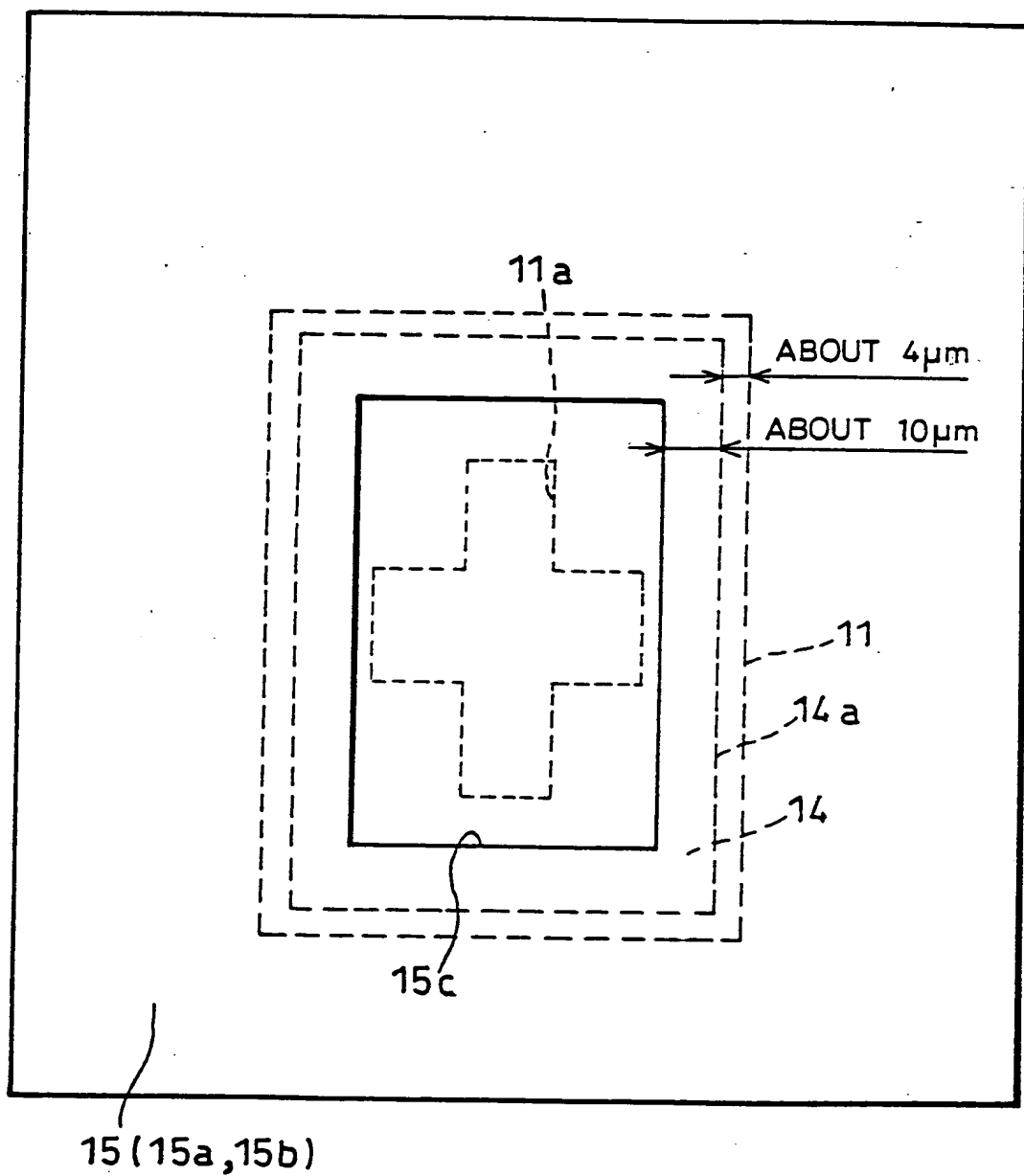


FIG. 3

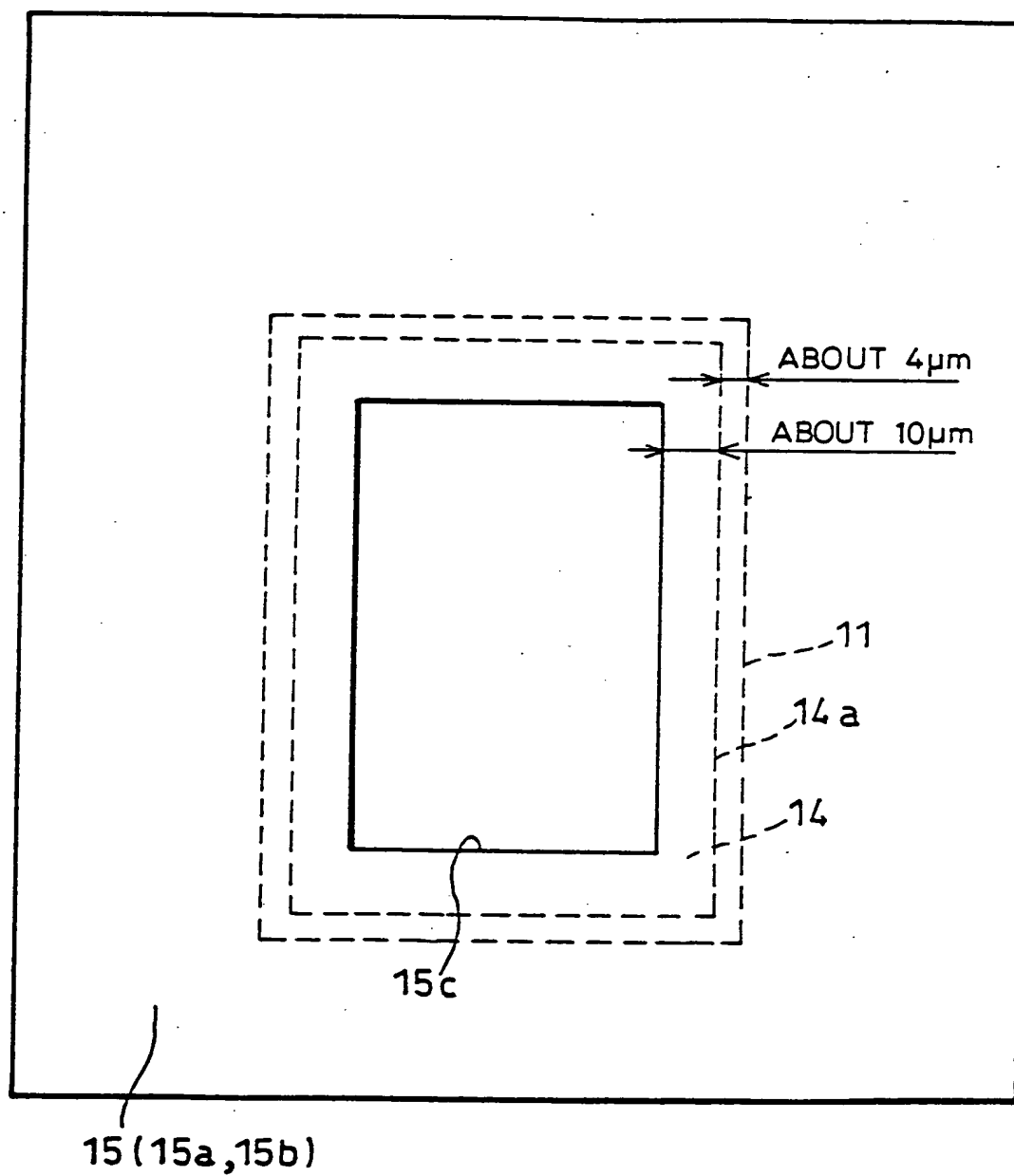


FIG. 4(a)

FIG. 4(b)

FIG. 4(c)

FIG. 4(d)

FIG. 4(d) is a cross-sectional view of a semiconductor device. It shows a substrate 11 with a base layer 6. On top of the base layer, there are two main regions, 8a and 8b, which are part of a larger structure 8. Above these regions, there are two sets of conductive layers, 9a and 9b, and 10a, 10b, and 10c respectively. The layers 10a, 10b, and 10c are grouped together by a bracket labeled 10. The entire structure is covered by a top layer 11.

FIG. 4(e)

FIG. 4(f)

FIG. 4(f) is a cross-sectional view of a semiconductor device. It shows a substrate with a series of layers: 10a (bottom), 10b, and 10c (top). A central region contains a structure with layers 11, 13, and 14. This central region is flanked by two side regions, 15a on the left and 15b on the right. A layer 12 is positioned above the central region and below the side regions. The layers are shown with different hatching patterns to indicate different materials or regions.

FIG. 5

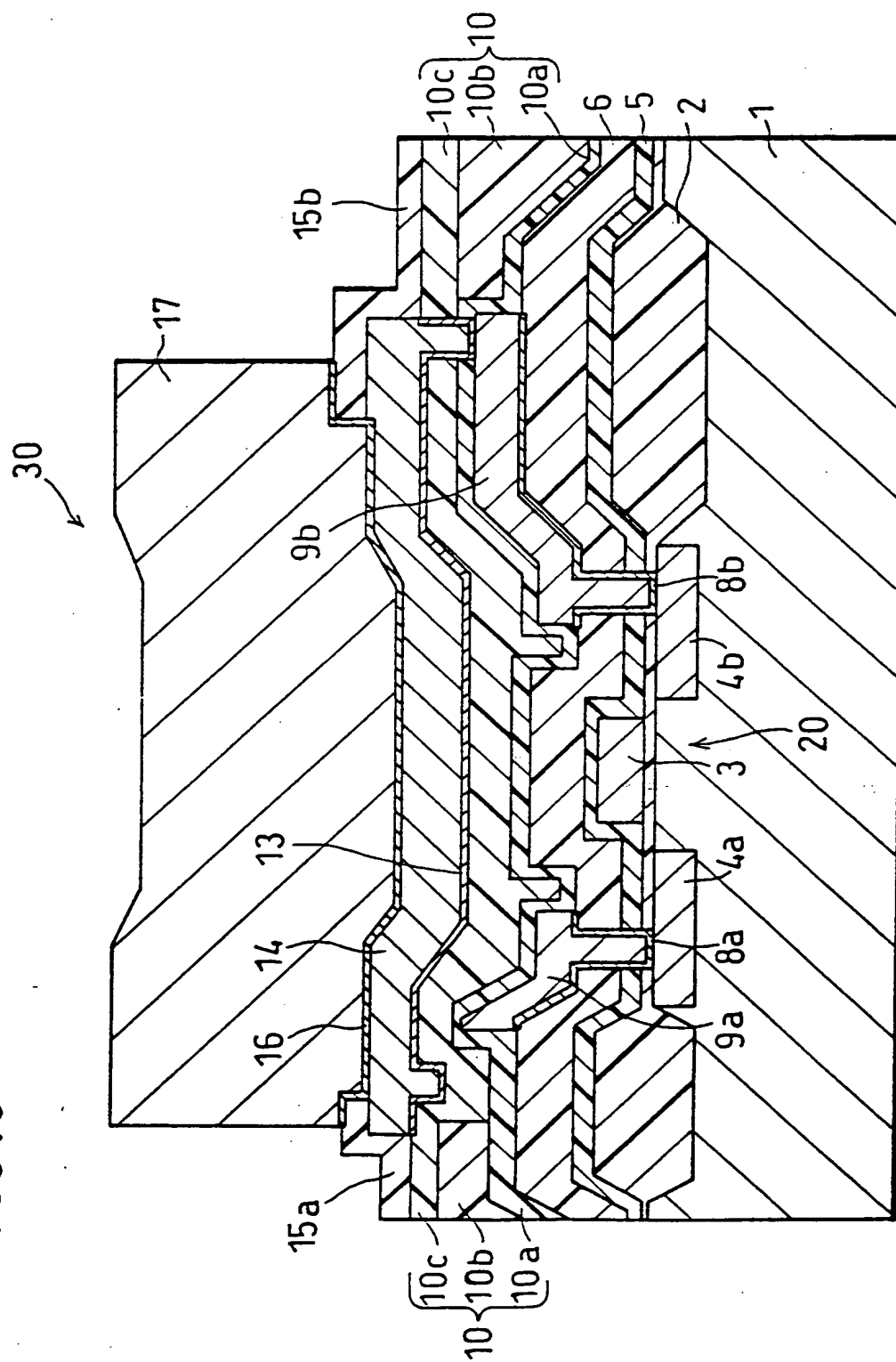
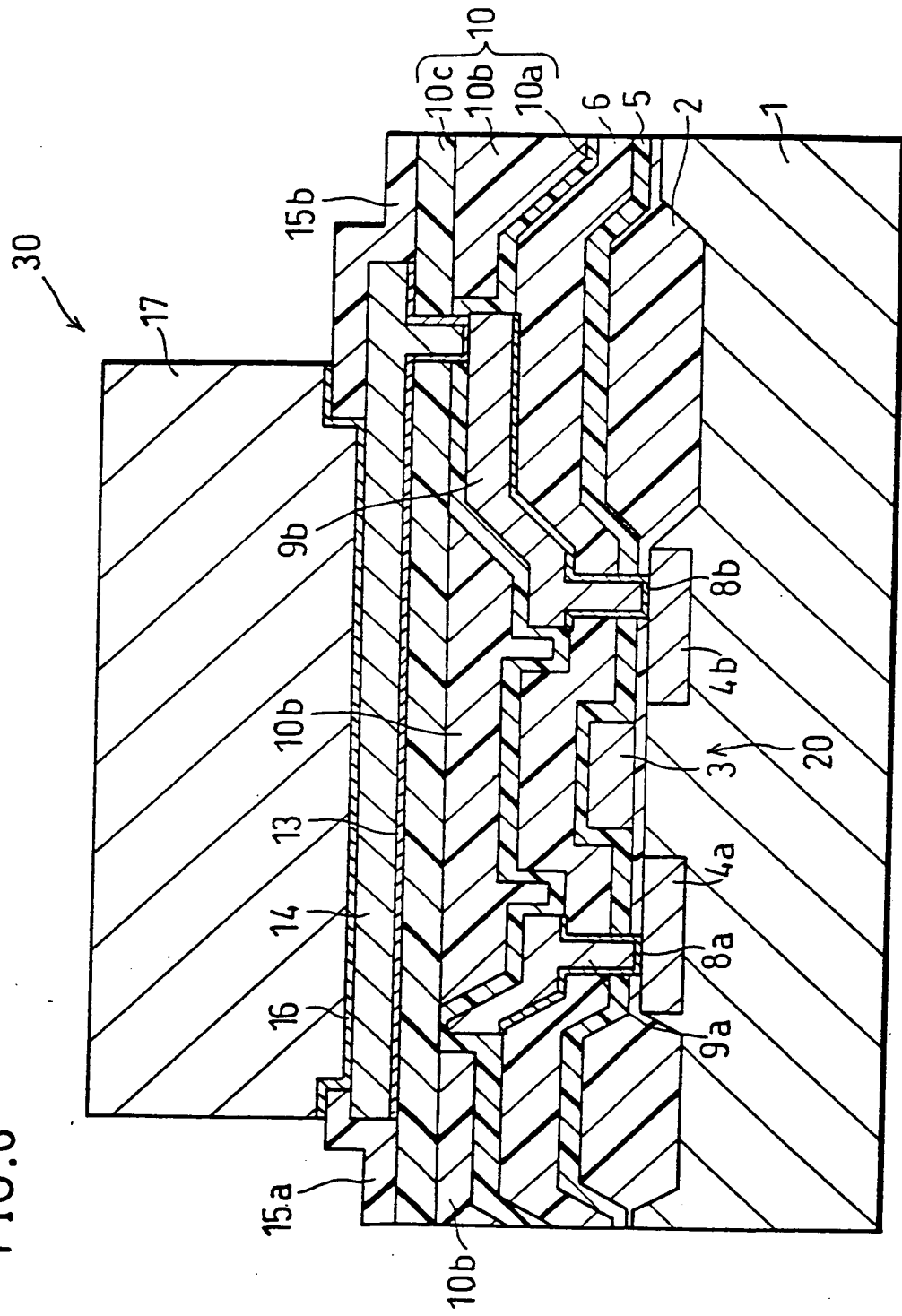


FIG.6



**FIG. 7**

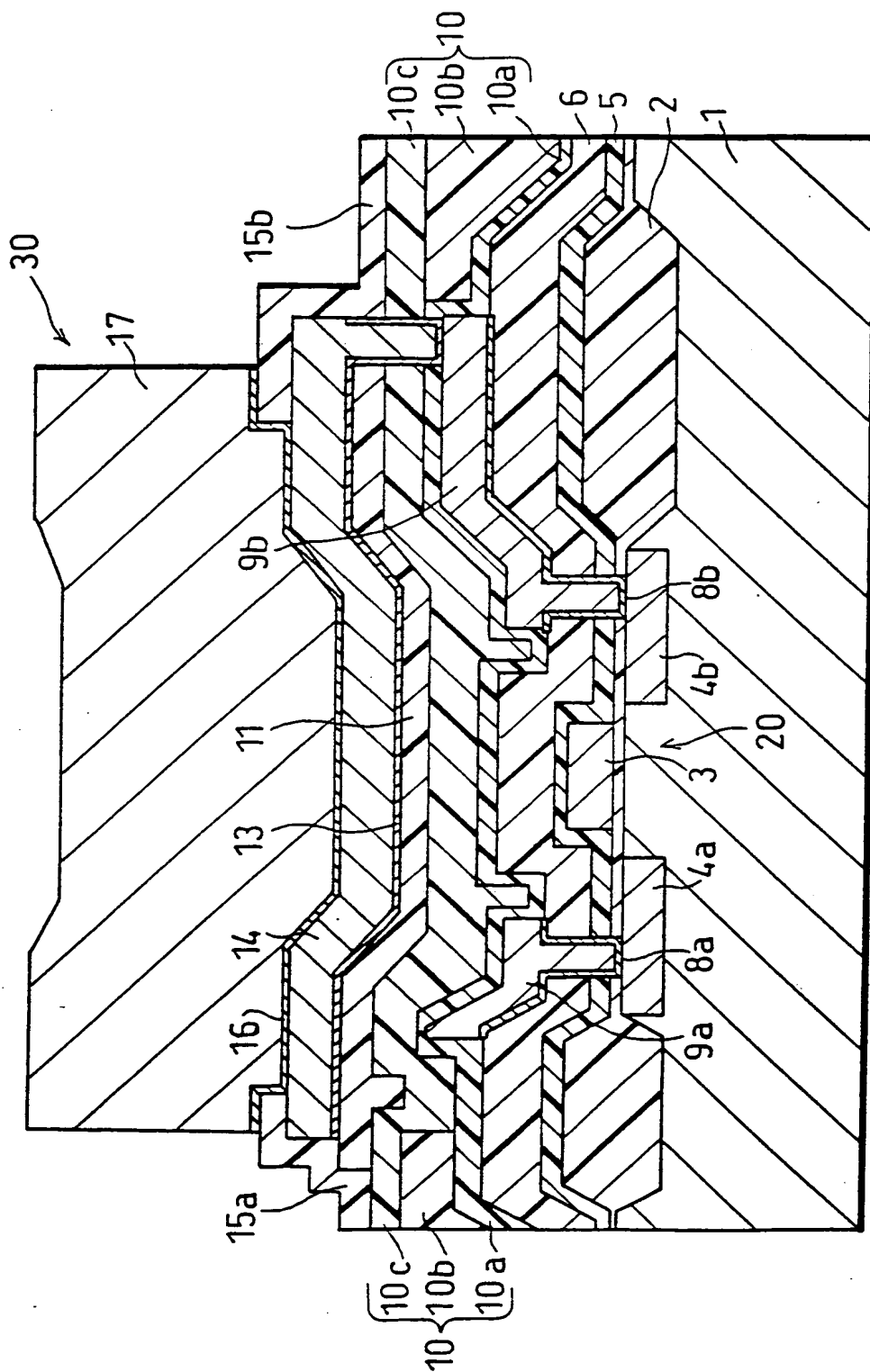






FIG.9

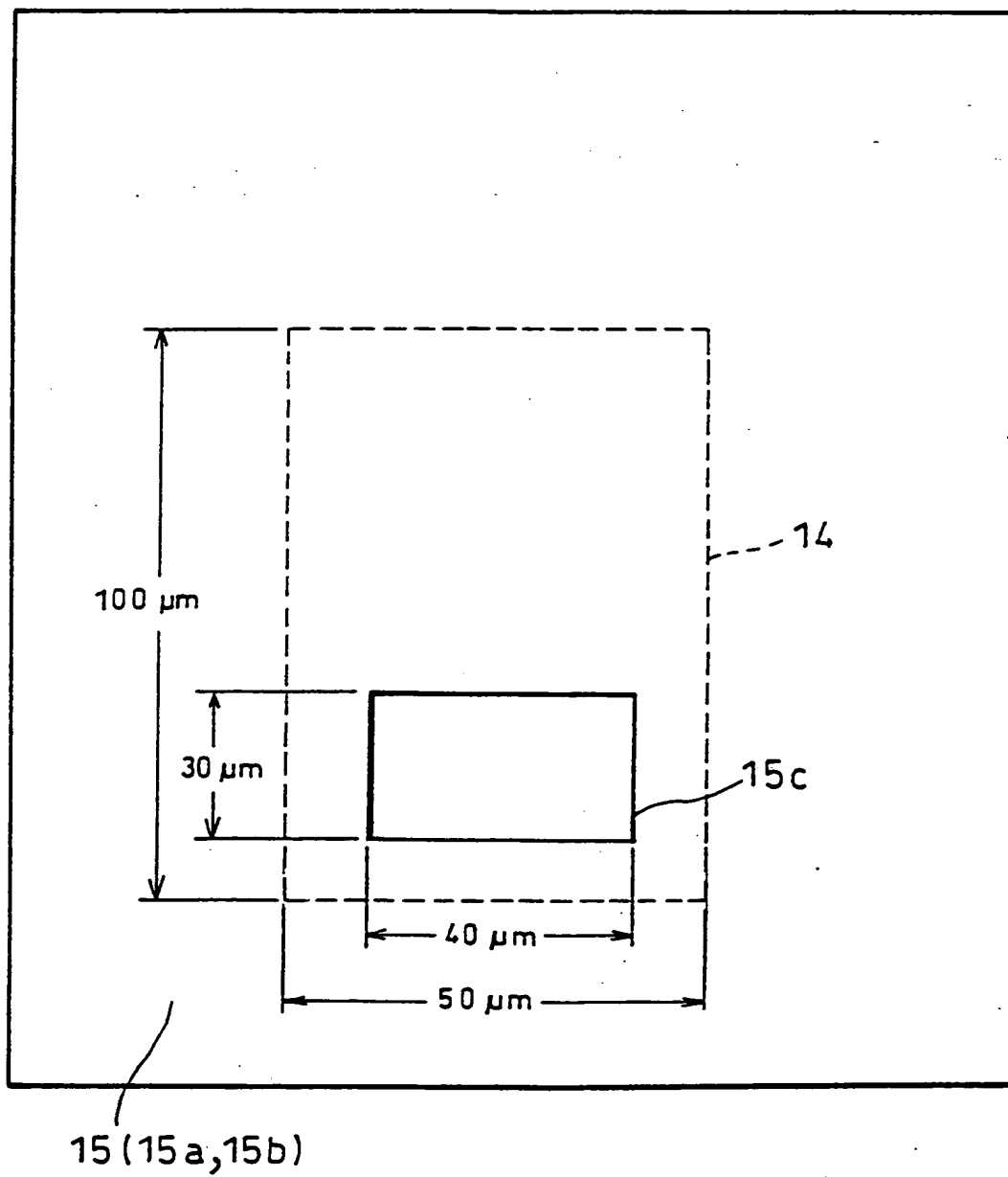
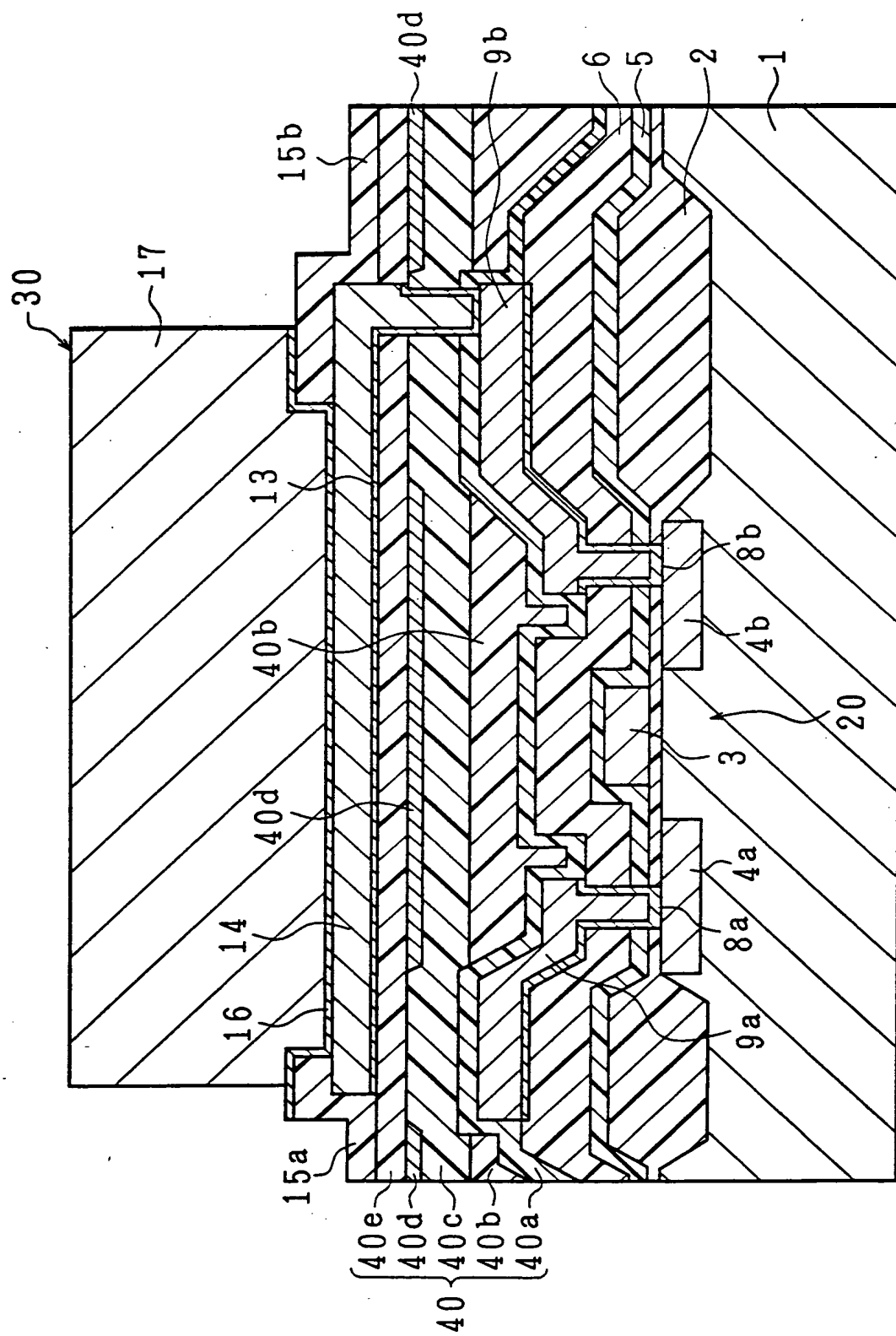


FIG. 10



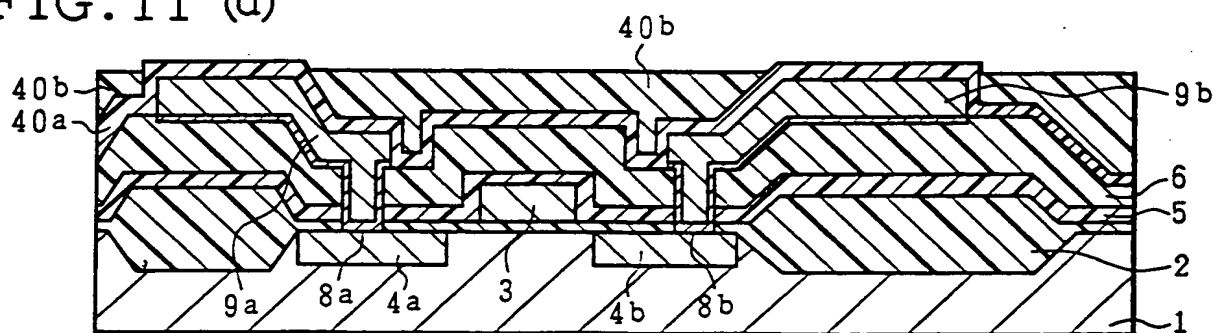
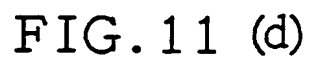
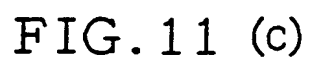
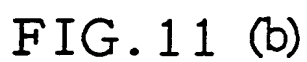
[illegible]

FIG. 12 (a)

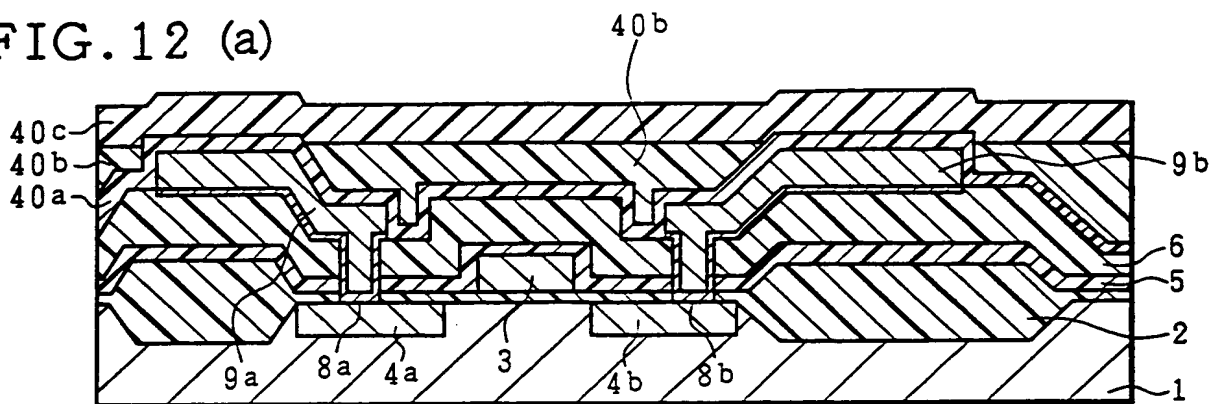


FIG. 12 (b)

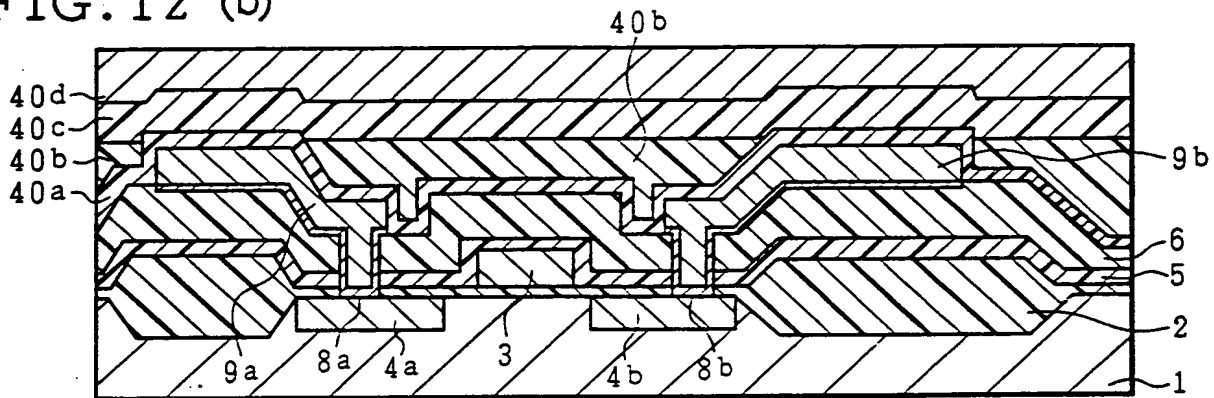


FIG. 12 (c)

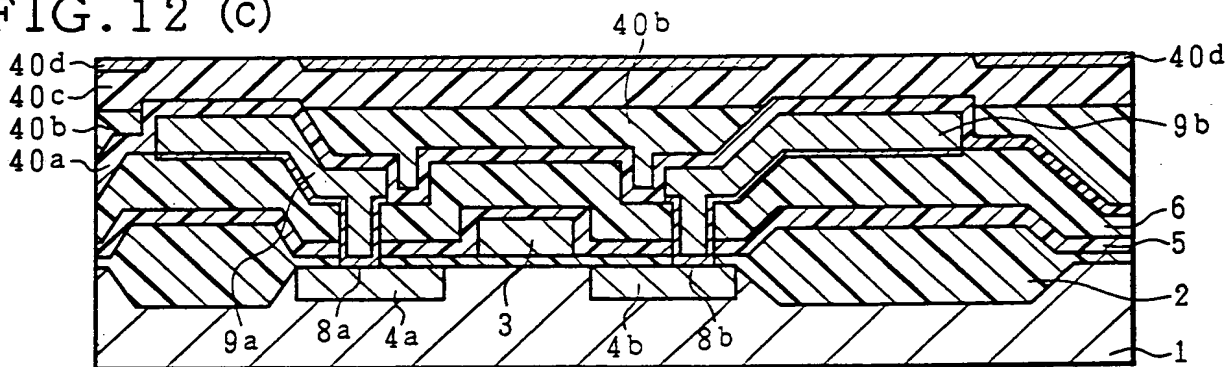
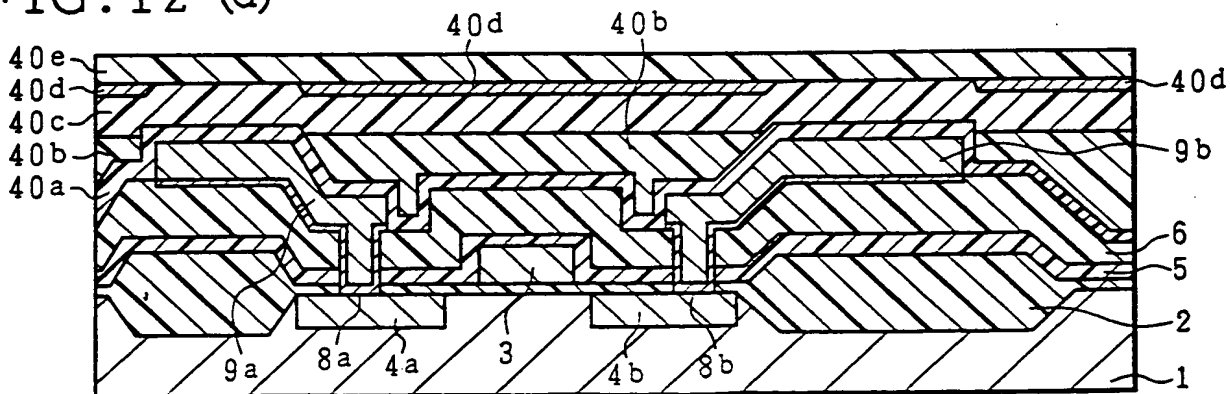


FIG. 12 (d)



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FIG. 13 (a)

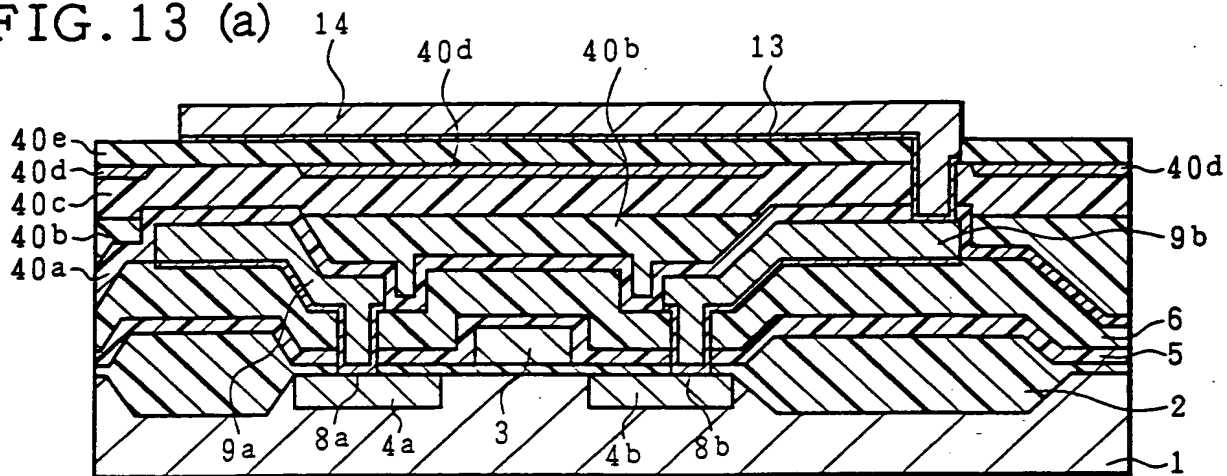


FIG. 13 (b)

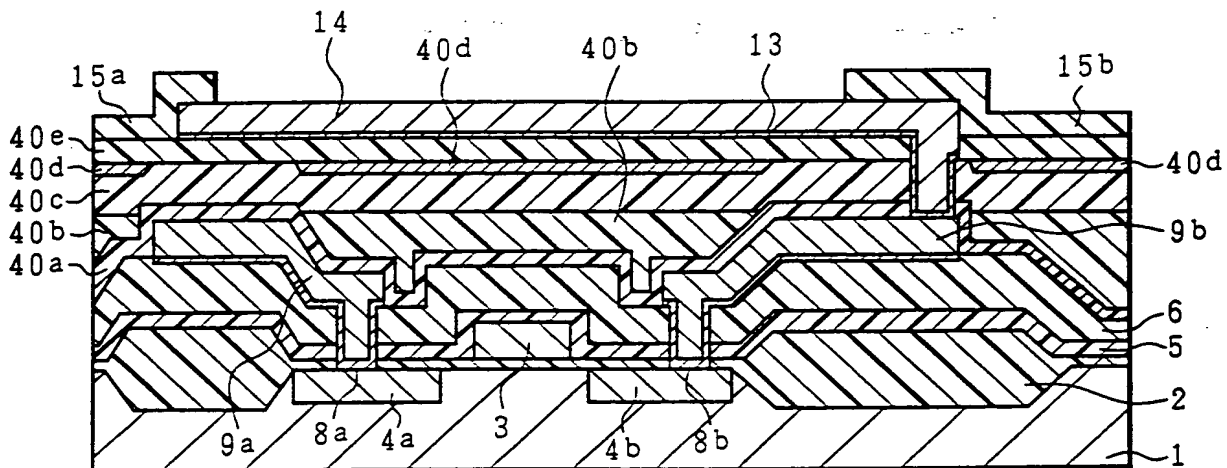


FIG. 13 (c)

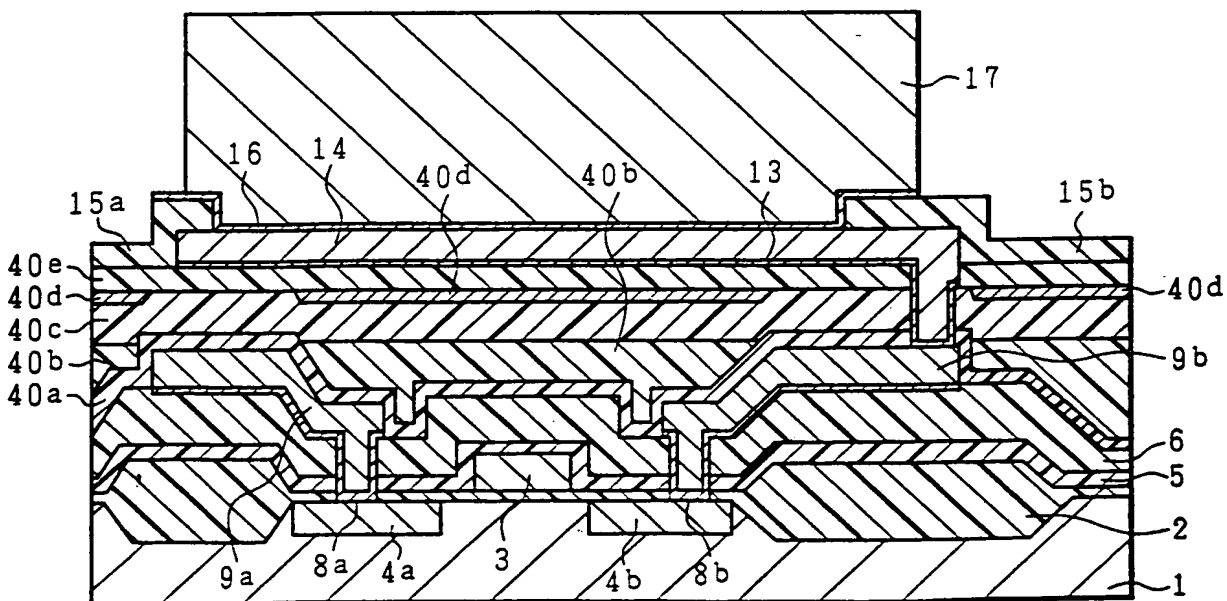


FIG. 14

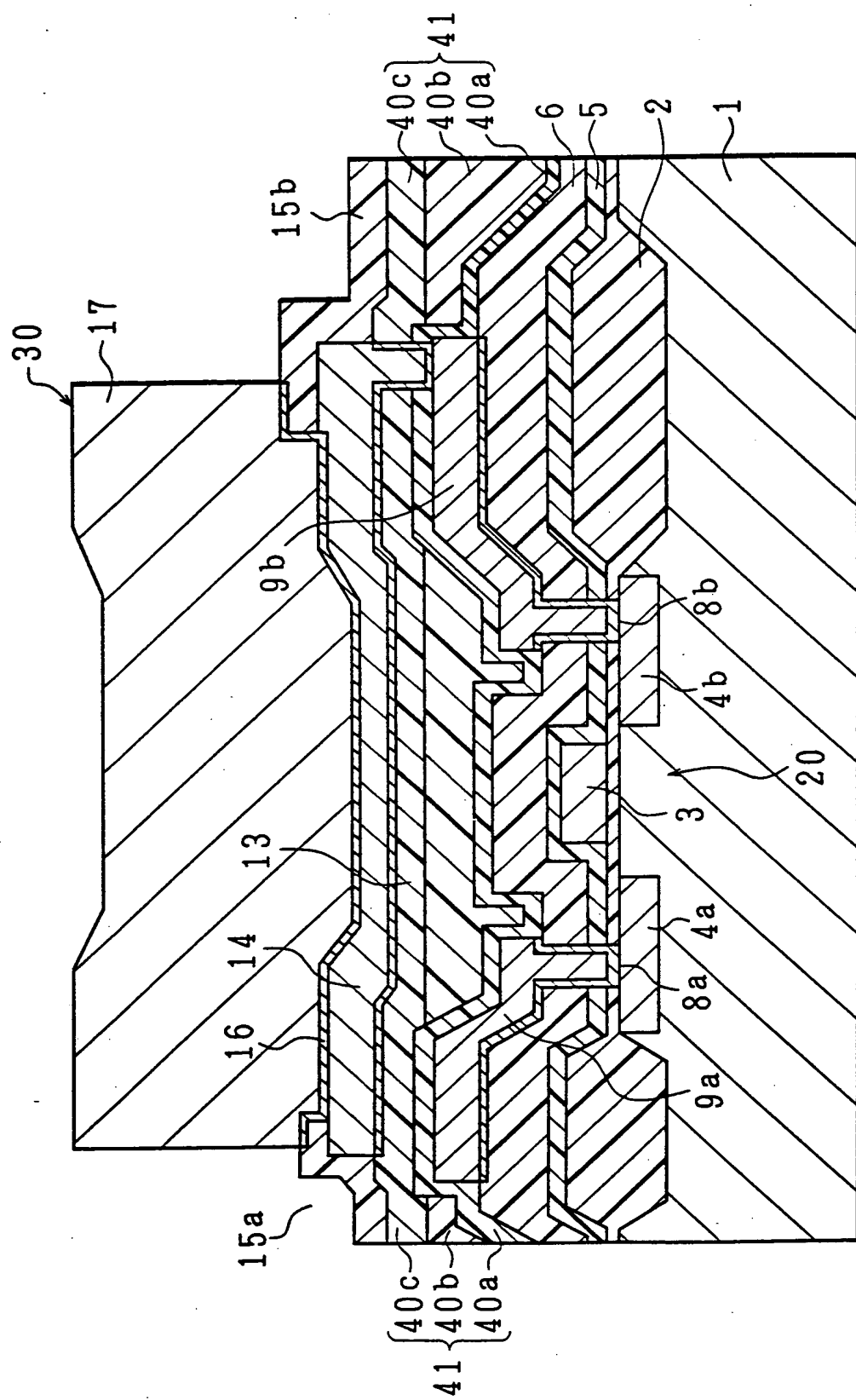


FIG. 15

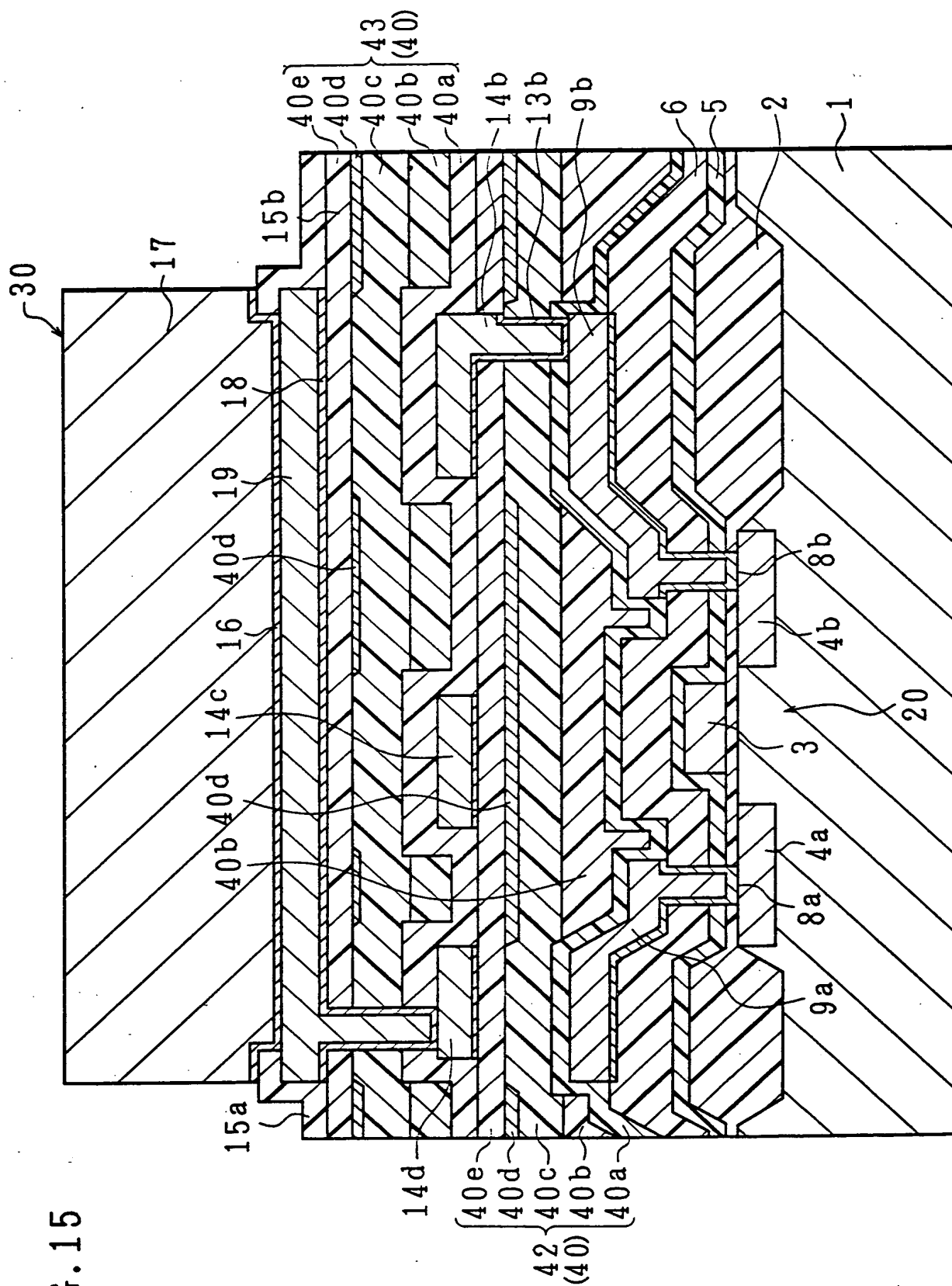
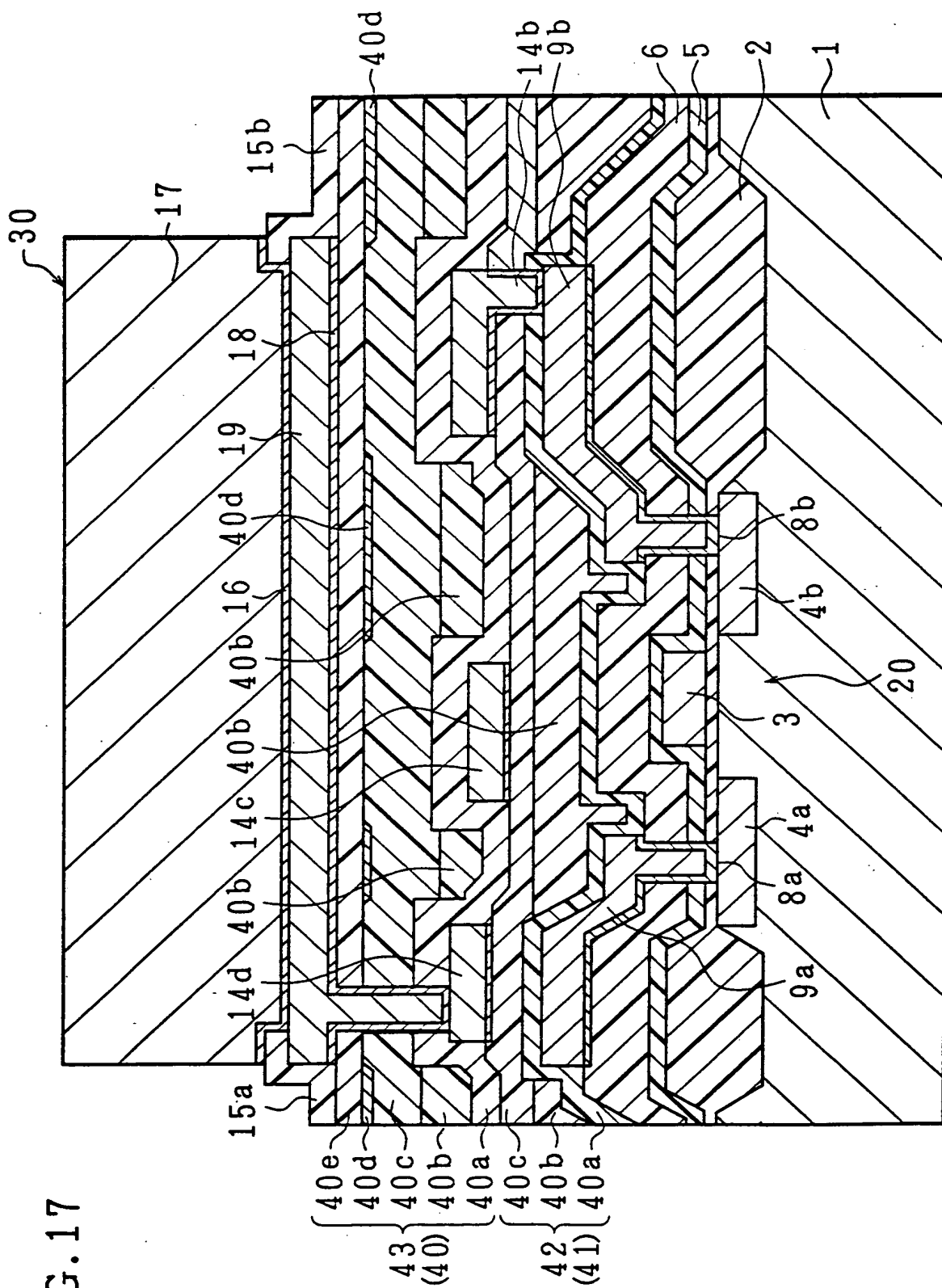
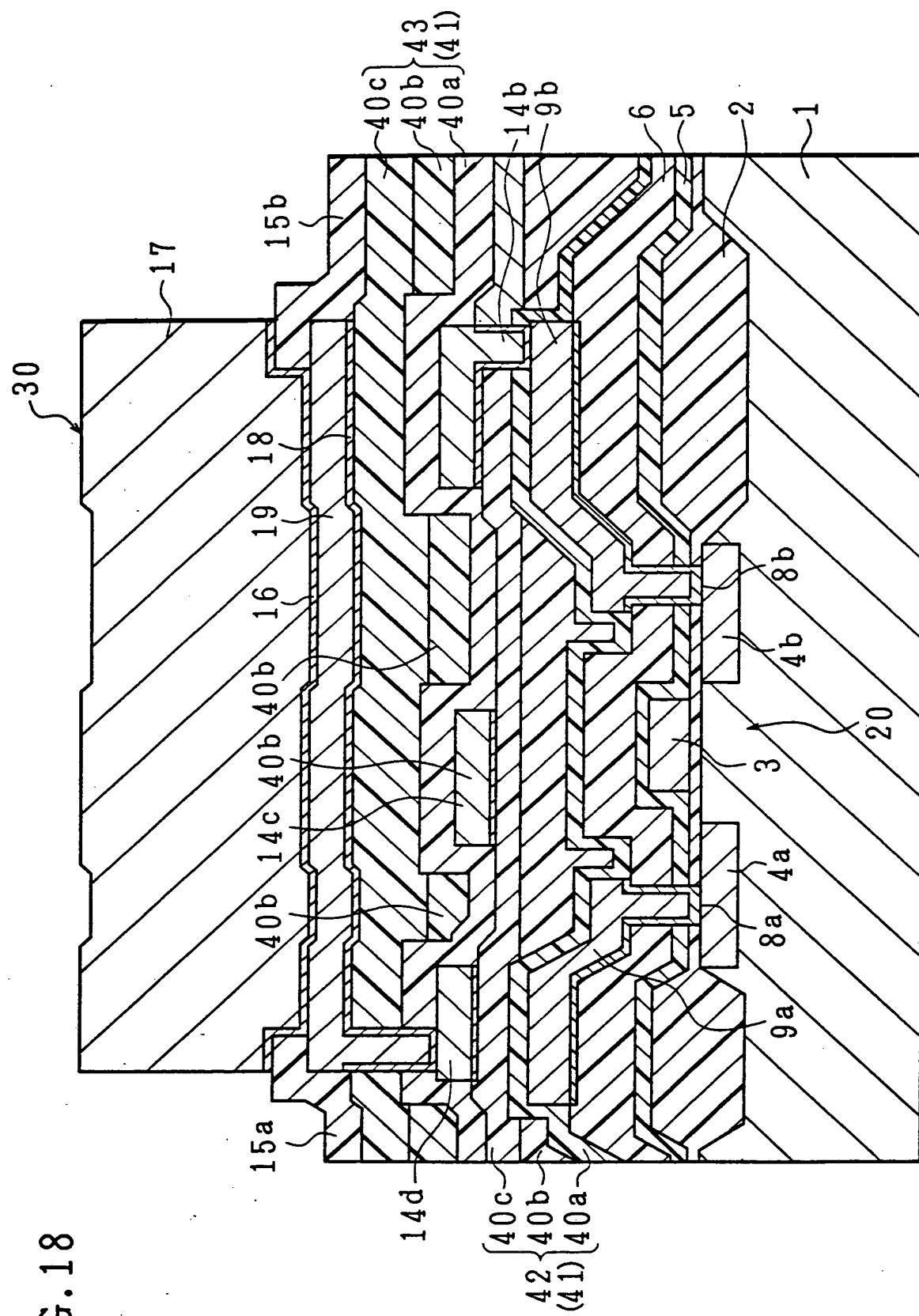






FIG. 17





**FIG. 19**

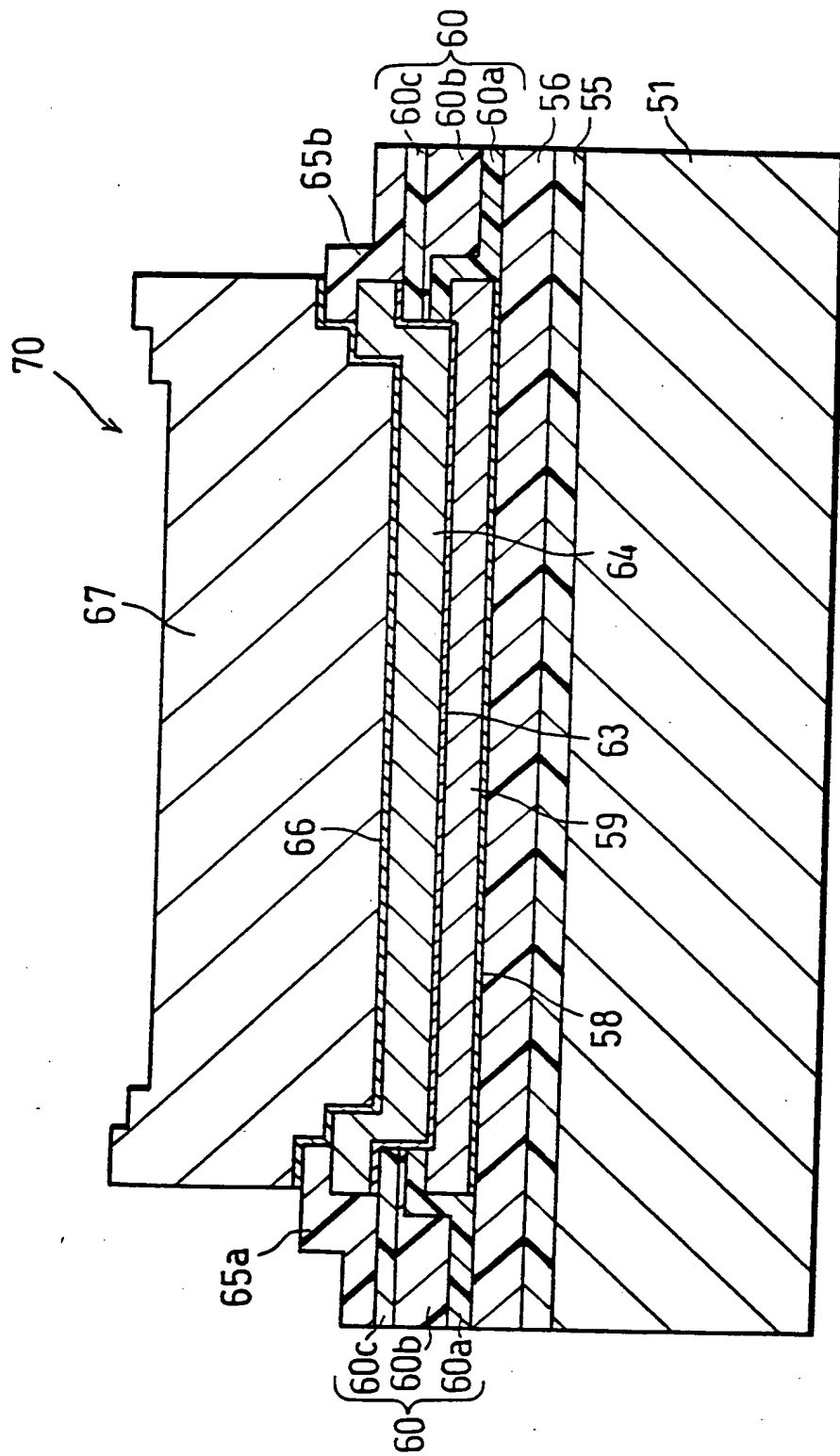
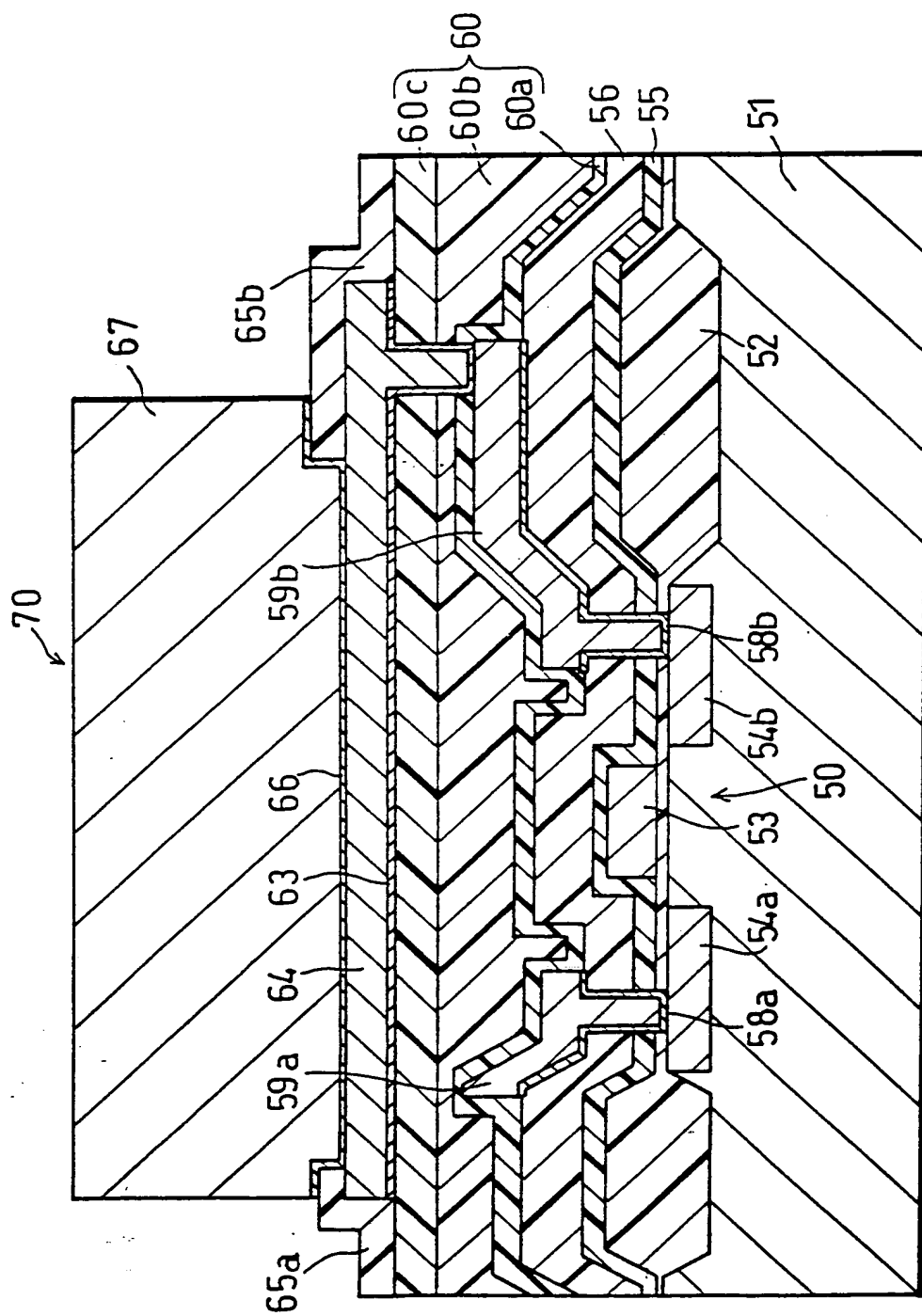


FIG. 20



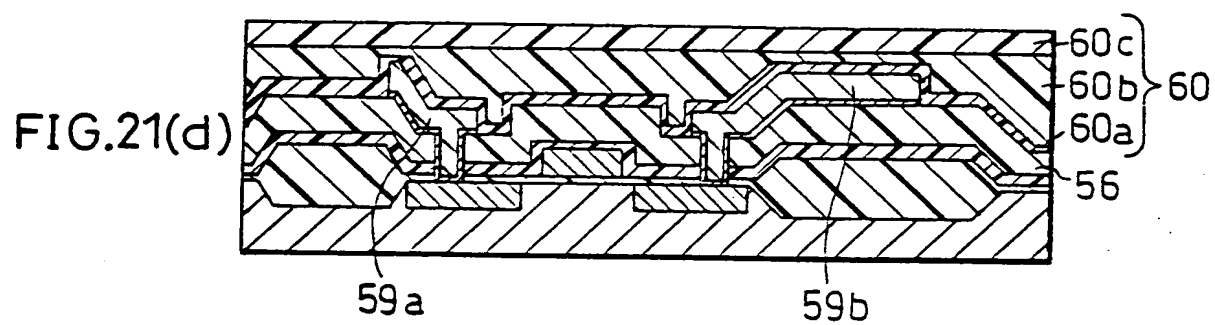
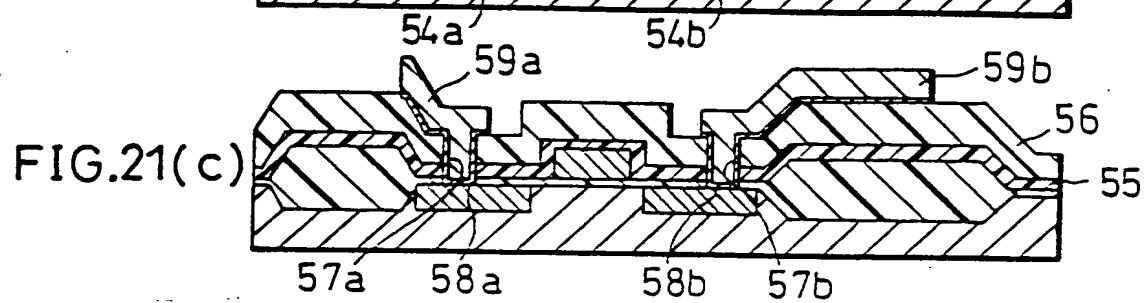
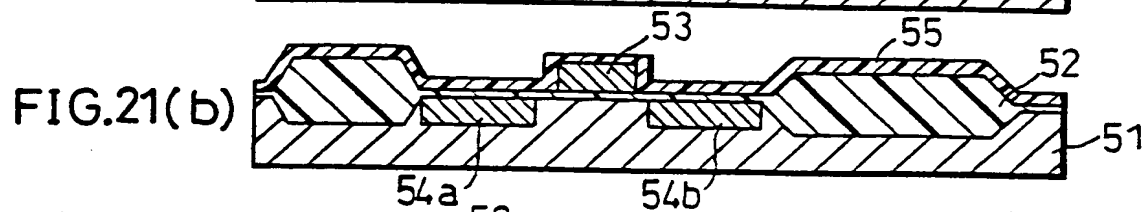
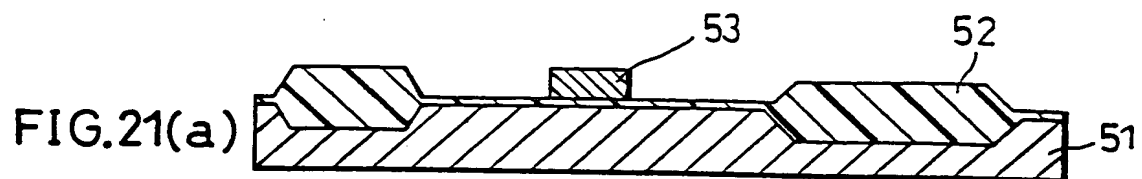


FIG. 22

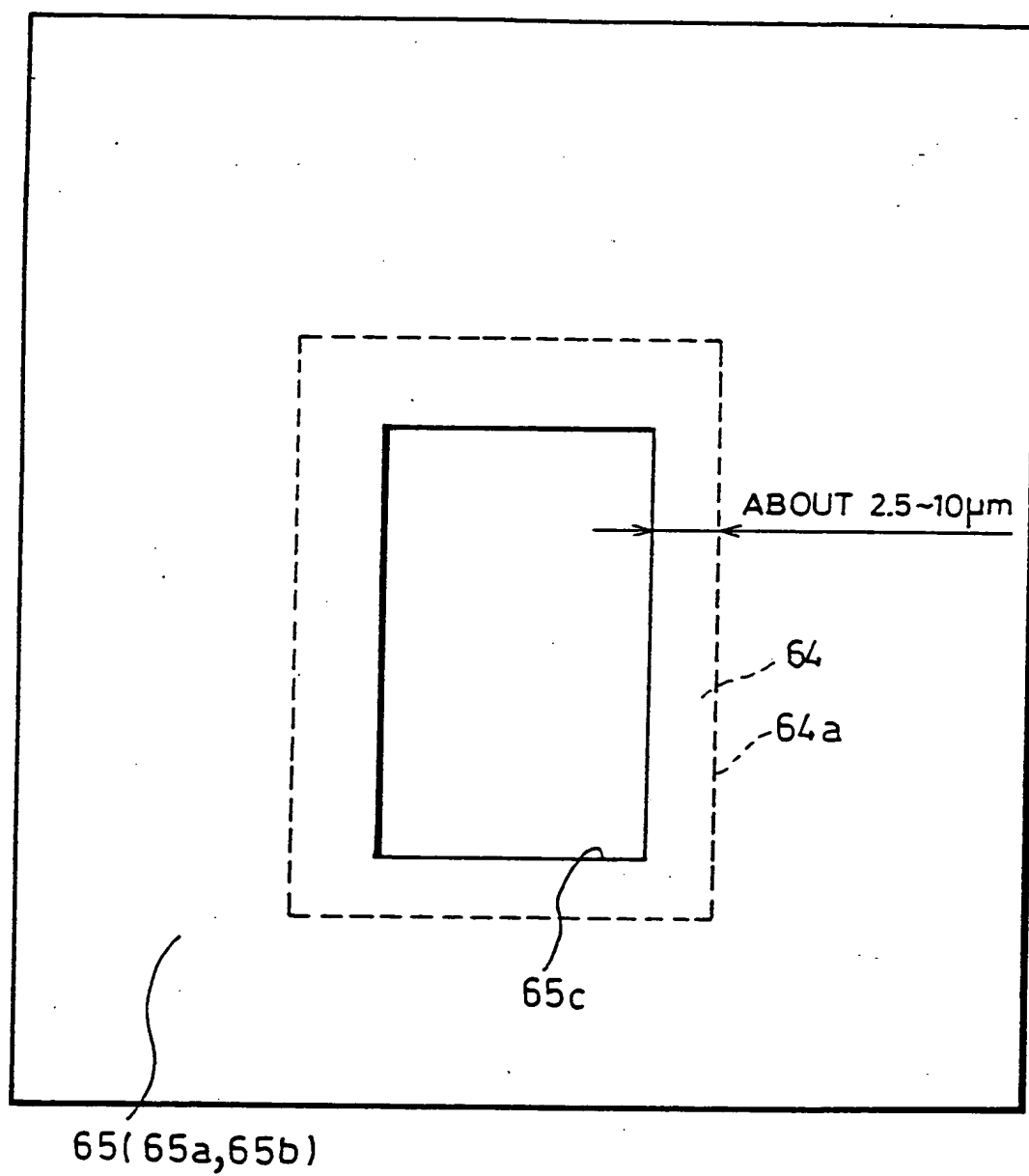


FIG. 23

